

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	1826993
<b>Application Number:</b>	10055241
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	8631
<b>Title of Invention:</b>	SYSTEM AND METHOD FOR CONFIGURING A RECONFIGURABLE SYSTEM
<b>First Named Inventor/Applicant Name:</b>	Joseph E. Peck
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<b>Filer:</b>	Jeffrey C Hood
<b>Filer Authorized By:</b>	
<b>Attorney Docket Number:</b>	5150-63500
<b>Receipt Date:</b>	31-MAY-2007
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<b>Application Type:</b>	Utility

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	NPL Documents	B2_Real-Time_Workshop_part-2.pdf	23621138	no	115
<b>Warnings:</b>					
<b>Information:</b>					
2	NPL Documents	B3_Guide_to_Rapid_Prototyping_with_Simulink_Real-Time_Workshop_and_dSpace.pdf	6356937	no	16
<b>Warnings:</b>					
<b>Information:</b>					
3	NPL Documents	B4_Real-Time_Interface_to_Simulink--RTI_30--Users_Guide.pdf	6293964	no	125
<b>Warnings:</b>					
<b>Information:</b>					
4	NPL Documents	B5_GORMAN_Real-Time_Data_Aquisition_and_Controls_using_Matlab.pdf	1355513	no	4
<b>Warnings:</b>					
<b>Information:</b>					
5	NPL Documents	B6_Alta_Group_of_Cadence_Design_Systems.pdf	8664189	no	34
<b>Warnings:</b>					
<b>Information:</b>					
6	NPL Documents	B9_Code_Generation_System--product_data_sheet.pdf	1205079	no	8
<b>Warnings:</b>					
<b>Information:</b>					
7	NPL Documents	B6_SPW-Matlab_Co-Simulation_Interface_Product_Data_Sheet.pdf	493590	no	2
<b>Warnings:</b>					
<b>Information:</b>					
8	NPL Documents	B7_Signal_Processing_Work_System--MatLab_Interface_User_Guide.pdf	18833215	no	72
<b>Warnings:</b>					
<b>Information:</b>					
9	Petition for review by the Office of Petitions.	Petition.pdf	109439	no	3
<b>Warnings:</b>					

<b>Information:</b>					
10	Information Disclosure Statement (IDS) Filed	IDS.pdf	236409	no	5
<b>Warnings:</b>					
<b>Information:</b>					
This is not an USPTO supplied IDS fillable form					
11	NPL Documents	B1_Choosing_Block-Diagram_Tools_for_DSP_Design.pdf	1239442	no	7
<b>Warnings:</b>					
<b>Information:</b>					
12	NPL Documents	B2_Real-Time_Workshop_part-1.pdf	6892139	no	115
<b>Warnings:</b>					
<b>Information:</b>					
13	NPL Documents	B13_Xanalog_Corporation_Sales_Manual.pdf	1337858	no	8
<b>Warnings:</b>					
<b>Information:</b>					
14	NPL Documents	B17_Xanalog--The_CAE_Workstation_comes_to_Simulation.pdf	683370	no	3
<b>Warnings:</b>					
<b>Information:</b>					
15	NPL Documents	B18_Xanalog-RT_Real_Time_Analog_and_Digital_I-O.pdf	829638	no	4
<b>Warnings:</b>					
<b>Information:</b>					
16	NPL Documents	B19_Xanalog_SC-plus.pdf	922520	no	5
<b>Warnings:</b>					
<b>Information:</b>					
17	NPL Documents	B10_SPW-CGS_Porting_Kits--product_data_sheet.pdf	523485	no	2
<b>Warnings:</b>					
<b>Information:</b>					
18	NPL Documents	B11_MultiProx_for_SPW--product_data_sheet.pdf	580362	no	4

<b>Warnings:</b>					
<b>Information:</b>					
19	NPL Documents	B12_DSP_ProCoder_for_SP W--product_data_sheet.pdf	1445990	no	4
<b>Warnings:</b>					
<b>Information:</b>					
20	NPL Documents	B14_Available_XA-1000_Lit erature_and_its_use.pdf	170743	no	2
<b>Warnings:</b>					
<b>Information:</b>					
21	NPL Documents	B15_XA-1000_Programming _ICONS.pdf	859391	no	15
<b>Warnings:</b>					
<b>Information:</b>					
22	NPL Documents	B16_Xanalog_CAE_System- The_Fastest_AT_Alive.pdf	257255	no	1
<b>Warnings:</b>					
<b>Information:</b>					
23	NPL Documents	B20_Xanalog_Specializing_i n_Workstations_for_Continu ous_Dynamic_Simulation.pdf	3606112	no	24
<b>Warnings:</b>					
<b>Information:</b>					
24	NPL Documents	B21_XANALOG_Real-time_ User_Guide.pdf	4826846	no	28
<b>Warnings:</b>					
<b>Information:</b>					
25	NPL Documents	B22_LEE_Gabriel--A_Desig n_Environment_for_Program mable_DSPs.pdf	1434134	no	13
<b>Warnings:</b>					
<b>Information:</b>					
26	NPL Documents	B23_LEE_A_Design_Tool_f or_HW_and_SW_for_Multi-p rocessor_DSP_Systems.pdf	682926	no	4
<b>Warnings:</b>					
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27	NPL Documents	B24_Gabriel_0-7_Overview. pdf	1085297	no	5

<b>Warnings:</b>					
<b>Information:</b>					
28	NPL Documents	B25_BUCK_Scheduling_Dynamic_Dataflow_Graphs_with_Bounded_Memory.pdf	1015999	no	4
<b>Warnings:</b>					
<b>Information:</b>					
29	NPL Documents	B26_LEE_Design_Methodology_for_DSP.pdf	28868	no	4
<b>Warnings:</b>					
<b>Information:</b>					
30	NPL Documents	B27_PINO_Interface_Synthesis_in_Heterogeneous_System-Level_DSP_Design_Tools.pdf	1163055	no	4
<b>Warnings:</b>					
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31	NPL Documents	B28_PINO_Software_Synthesis_Single-Processor_DSP_Systems_Using_Ptolemy.pdf	8820417	no	48
<b>Warnings:</b>					
<b>Information:</b>					
32	NPL Documents	B29_KALAVADE_A_Hardware-Software_Codesign_Methodology_for_DSP_Applications.pdf	4494794	no	12
<b>Warnings:</b>					
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33	NPL Documents	B30_PINO_Automatic_Code_Generation_for_Heterogeneous_Multiprocessors.pdf	1361302	no	4
<b>Warnings:</b>					
<b>Information:</b>					
34	NPL Documents	B31_BINDRA_Tool_Chest_Continues_to_grow.pdf	821684	no	2
<b>Warnings:</b>					
<b>Information:</b>					
35	NPL Documents	B32_PINO_Mapping_Multiple_Independent_Synchronous_Dataflow_Graphs.pdf	1813923	no	6
<b>Warnings:</b>					
<b>Information:</b>					
36	NPL Documents	B33_KALAVADE_Hardware-Software_Co-Design_Using_Ptolemy.pdf	4336891	no	18

<b>Warnings:</b>					
<b>Information:</b>					
37	NPL Documents	B34_PINO_Software_Synthesis_for_DSP_using_Ptolemy.pdf	3291896	no	15
<b>Warnings:</b>					
<b>Information:</b>					
38	NPL Documents	B35_Ptolemy_0-7_Users_Manual_1-175.pdf	23554679	no	175
<b>Warnings:</b>					
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39	NPL Documents	B35_Ptolemy_0-7_Users_Manual_176-351.pdf	20215271	no	176
<b>Warnings:</b>					
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40	NPL Documents	B35_Ptolemy_0-7_Users_Manual_352-532.pdf	21937194	no	181
<b>Warnings:</b>					
<b>Information:</b>					
41	NPL Documents	B36_i-Logix_Product_Overview.pdf	6832731	no	44
<b>Warnings:</b>					
<b>Information:</b>					
42	NPL Documents	B37_I-Logix_StateMate_MAGNUM_supports_PCs.pdf	581932	no	2
<b>Warnings:</b>					
<b>Information:</b>					
43	NPL Documents	B38_I-Logix_Signs_Reseller_Agreement_for_VAPS.pdf	451944	no	2
<b>Warnings:</b>					
<b>Information:</b>					
44	NPL Documents	B39_I-Logix_Introduces_C_Rhapsody_Tool.pdf	308098	no	2
<b>Warnings:</b>					
<b>Information:</b>					
45	NPL Documents	B40_I-Logix_StateMate-C_StateMate_Product_Family.pdf	639980	no	4

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<b>Information:</b>					
46	NPL Documents	B41_I-Logix_and Integrated Systems_Link_Statemate_Magnum_and_Matrix_Autocode.pdf	286980	no	2
<b>Warnings:</b>					
<b>Information:</b>					
47	NPL Documents	B42_I-Logix_and_Wind_River_unveil_Industrys_First_Rapid_Prototyping_Solution.pdf	299762	no	3
<b>Warnings:</b>					
<b>Information:</b>					
48	NPL Documents	B43_I-Logix-Inc_Endorses_Unified_Modeling_Language.pdf	297398	no	1
<b>Warnings:</b>					
<b>Information:</b>					
<b>Total Files Size (in bytes):</b>			197101779		
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p> <p><b><u>New Applications Under 35 U.S.C. 111</u></b>          If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p> <p><b><u>National Stage of an International Application under 35 U.S.C. 371</u></b>          If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p> <p><b><u>New International Application Filed with the USPTO as a Receiving Office</u></b>          If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.</p>					